Model Check What You Can, 
Runtime Verify the Rest

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Abstract

Model checking and runtime verification are pillars of formal verification but for the most part are used independently. In this position paper we argue that the formal verification community would be well-served by developing theory, algorithms, implementations, and applications that combine model checking and runtime verification into a single, seamless technology. This technology would allow system developers to carefully choose the appropriate balance between offline verification of expressive properties (model checking) and online verification of important parts of the system’s state space (runtime verification). We present several realistic examples where such technology appears necessary and a preliminary formalization of the idea.

1 Introduction

The virtues and drawbacks of model checking (MC) are well known: model checked systems undeniably satisfy some important properties, but some systems are too unwieldy for model checking in practice. Likewise the virtues and drawbacks of runtime verification (RV) are well known: most systems can be runtime verified, but that verification only ensures that some important properties hold for that fraction of the system that happens to be exercised during verification. In short, model checking guarantees completeness at the cost of applicability and runtime verification guarantees applicability at the cost of completeness. But if there were techniques for integrating model checking and runtime verification (MC+RV), system developers would have fine-grained control over the tradeoff between completeness and applicability during the verification process.

Besides the intellectual gratification of hybridizing two well-known techniques for verification, MC+RV would allow the verification community to present a unified front to the outside world—to provide consumers of verification technology with a single conceptual product to understand and apply. Improving the accessibility of verification technology to the outside world will help both society as a whole, since software will contain fewer bugs, and the research community in particular because of the increased source of real-world verification problems encountered in the wild.

The technical problems arising when combining model checking and runtime verification are mainly due to their substantially different underlying assumptions. Model checking runs offline (i.e., before the system is deployed), and runtime verification runs online (i.e., after the system is deployed). Model checking only works properly when the entire system can be examined, and runtime verification only requires a single run of the system. These differences are of course what makes the hybridization of the two techniques so attractive.

These basic observations about the potential benefits of MC+RV are not novel in and of themselves. [7] employed static program analysis (of which MC is a special sort) to simplify runtime verification checks. [20] utilized the combination of model checking and runtime verification to identify security vulnerabilities in web applications. [8] utilizes runtime analysis to
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perform an anytime variant of static analysis. While such examples of combining static and dyna-
mic analysis exist, they are not based on a single, unified theory of combining these forms of
analysis and require intimate knowledge of the underlying analysis (or verification) technology.

In this position paper, we advocate investigating the underlying theory of combining model
checking and runtime verification. That theory will enable tools where a developer chooses
the extent of verification appropriate to the application—to prioritize how mission critical each
portion of a system is and to verify that system accordingly. In short, we argue that verifica-
tion technology will become commonplace only when that technology accounts for the limited
resources (time, compute cycles, etc.) organizations have to devote to verification.

In the remainder of this paper, we begin by comparing the strengths and weaknesses of
model checking and runtime verification in more detail (Section 2). We then state the position
this paper puts forth (Section 3) and identify several domains where the combination of model
checking and runtime verification appears to offer substantial benefit (Section 4). Then we
briefly discuss a formalization of the central problem of MC+RV (Section 5) and finish with
related work (Section 6) and a conclusion (Section 7).

2 Model Checking and Runtime Verification

Model Checking. Model checking is a technique where one takes a model of a system and
a model of a property and algorithmically checks whether the system satisfies the property
[10, 11, 27]. Typically, the systems one has in mind are hardware or software systems, and the
specifications are temporal. While model checking is commonly applied to finite-state systems,
at times it is possible to apply it for infinite-state systems using, for example, abstractions,
symmetry properties, and small-model properties (e.g., [1, 14, 21, 25]). Model checking can
be explicit-state where both model and properties (or their complements) are represented as
automata-like structures, and model checking reduces to finding whether any path in the system
violates the property. Of course, when liveness properties are concerned, one must consider only
fair paths. An alternative approach to model checking is using symbolic techniques (most often,
BDD- or SMT- based). Each approach has its strengths. For example, all systems we are aware
of that model checked timed properties employ an explicit-state approach.

The attraction of model checking is that it is algorithmic, the alternative being the use
of deductive methods and theorem provers (assuming that one wishes the verification to be
at least automatically checked). However, whichever model checking approach one employs,
state-explosion remains the main hurdle of using model checking for large systems. Much work
has been devoted to pushing the envelope, to the extent that some infinite state systems have
been model checked. Yet, it is still often the case, especially with software, that the state space
is too large and no known reduction in the state space removes this problem. This is the reason
that model checking is mainly used as a debugging mechanism in the early design stages and
methods such as bounded model checking [6] have gained popularity.

Another drawback of model checking techniques is that they are not easily amenable to
compositionally. While much work as been done on compositional model checking [21], the
application of such methods in “full-fledged” software systems is still impractical. This is
important since a single system often includes several components that can be swapped out for
similarly behaving components, and one may wish to verify the system without the swappable
components independently from the components themselves. To complicate matters further,
some of the swappable components may be black boxes. Lack of model checking compositionality
is also problematic for large (e.g., infinite state) systems where there is a good reason to believe
that during most of its executions the system visits only a small number of states, and this

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portion of the system can be model-checked.

For these and other reasons (some of them will be elaborated on in the sequel) one may want to use model checking to verify only parts of a system and to use other methodologies to guarantee the correctness of other parts. We propose to use Runtime Verification for this purpose.

**Runtime Verification.** In contrast to model checking, runtime verification monitors a system and extracts information from its executions to detect (and, sometimes, react to) flaws. While the term runtime verification is only a decade old, the ideas behind runtime verification had been used long before. Recently, runtime verification has become an active research area because of its power to ensure correctness of systems that are not amenable to model checking. In particular, while model checking is mostly useful at the design stage, runtime verification is useful at the deployment stage [3,4,12,19].

The main technical problems in runtime verification are (i) automatically adding runtime verification code to an existing system and (ii) minimizing the overheads of that code. A popular approach for the former is to use aspect-oriented programming techniques that allow such code to be written independently of the original system and through compiler techniques automatically injected into the code [2]. For the second problem, some work has employed static analysis to eliminate any runtime checks that can easily be identified as unnecessary [7].

In contrast to model checking, runtime verification inherently performs well when exploring only a portion of the state space—it was designed to do so. But its overall guarantees are much weaker than those offered by model checking. The properties that are checked need only hold over the portion of the state space that are executed once the system is deployed, and the class of properties amenable to runtime verification is a strict subset of the properties amenable to model checking. Thus while runtime verification is better behaved on partial state space explorations, it offers weaker guarantees of completeness (both in terms of expressiveness and state-space coverage).

### 3 Position

Both runtime verification and model checking are powerful techniques for checking correctness of systems. Table 1 represents a rough comparison of the methods.

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<tr>
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<th>Model Checking</th>
<th>Runtime Verification</th>
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<td>Entire State Space</td>
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**Table 1: Model Checking vs. Runtime Verification**

Our position is that a theory for combining model checking and runtime verification offers numerous advantages and advances the community’s long term goal of making both hardware and software system verification ubiquitous. Our premise is that it is often relatively easy to identify a set of states where model checking techniques can be employed. This set of space can then be model checked. Once a system enters a state outside of this set, a runtime monitor can be invoked. Of course, one has to monitor whether or not the system’s state is in or out of this set. Thus, the test whether or not the system is in a state that is in the set has to be simple, or otherwise, the advantage of model checking states in the set is compromised. In the
sequel we give examples of several systems/properties where MC+RV is beneficial. Below we describe MC+RV in terms of the three evaluation criteria from Table 1.

Completeness: Model checking is concerned with the entire state space of the system, whereas runtime verification is only concerned with the portion of the state space that is relevant to a particular deployment. By combining model checking and runtime verification the state space explored is somewhere in between the entire space and the deployment-relevant space.

Applicable Properties: Model checking allows us to verify temporal properties of a system whereas runtime verification is usually limited to safety properties. A hybrid of the approaches would allow us to verify all that runtime verification can outside of the model checked state space, and all that model checking can in it.

Performance Penalty: The performance penalty for model checking occurs offline (i.e., before deployment), whereas runtime verification pays an online performance penalty. The performance penalty of the hybrid approach is a developer-controlled combination of offline and online.

We envision the results of this theory embodied within a tool that allows a developer to choose the extent to which model checking is performed, allowing runtime verification to handle the remaining system. Our message to developers is simple: model check what you can, and runtime verify the rest.

4 Examples

Here we present a number of examples illustrating why combining model checking and runtime verification (MC+RV) is useful for verifying real-world systems. For each example, we ask the questions:

- What portion of the space should we model check and which portion should we runtime verify?
- What formula are we model checking, and what formula are we runtime verifying?

4.1 Contracts

In the context of programming languages, a software contract is a logical specification dictating how a piece of software is supposed to behave [22]. For example, a Heap is a data structure that stores an ordered set of elements. The contract for the Heap might require that inserting an element produces a heap that represents the same set as before the insert but with the new element added; merging two heaps results in a heap with the union of the original heaps’ elements.

In theoretical computer science, researchers will often introduce a new algorithm and then prove its correctness. Those proofs of correctness usually assume the data structures they utilize behave as they are supposed to, e.g., the Heap obeys its contract. The benefit to this style of proof is that any implementation of those data structures that satisfies their contracts can be used without violating the correctness of the overall algorithm. For example, [15] describes an algorithm for curve finding useful for computer vision that utilizes a Heap and proves correctness as well as runtime complexity assuming the Heap satisfies its contract and complexity bounds.
Partitioning the proof of an algorithm’s correctness into two phases (algorithm and data structures) points to an application of MC+RV. For systems relying on data structures, we can model check the correctness of those systems assuming the data structures behave according to their contracts. We can then independently verify the contracts of the data structures. For those situations where offline contract verification is impractical, we can runtime verify the contracts, which is functionality being built into modern programming languages. [16] describes an implementation of runtime contract verification that for certain properties preserves the (amortized) asymptotic behavior of the data structures.

In this example, it is clear what portion of the system ought to be model checked and what portion should be runtime verified. It is also clear what formula we intend to model check and what formula we should runtime verify: if the data structure’s contract is \( \alpha \) and the overall property of the system we want to verify is \( \beta \) then we model check \( \alpha \rightarrow \beta \) and we runtime verify \( \alpha \). At runtime, the verifier is turned on only during the data structure operations.

4.2 Race Conditions

Race conditions occur in multi-threaded software when the integrity of data depends on the order in which different threads access that data. For example, if Alice and Bob both try to withdraw the last $100 from their joint bank account, and there is some way for them both to succeed, then there is a race condition in the bank’s software.

The authors of [13] outfitted Java with a runtime race condition detector and found that the overheads could be an order of magnitude or more slowdown of the software. This performance penalty is likely too high to warrant automatic race condition detection in production systems, especially when the race conditions are rare. However, if we could verify that race conditions do not occur “most of the time” via model checking, we might be willing to pay such overheads for runtime race condition detection in those rare situations when they might occur.

For example, suppose that the bank software correctly handled the situation where Alice and Bob were both simultaneously trying to withdraw their last $100 but not the case where Alice, Bob, and their son Charlie were all simultaneously trying to withdraw the last of their money. If we could verify offline that the software performed correctly in the case of two competitors, we might pay for an order of magnitude slowdown in that exceedingly rare situation when three or more competitors accessed the system simultaneously.

Since the state space of the system grows exponentially with the numbers of competitors, one would wish to model check on as few competitors as possible. Yet, if there are good reasons to believe that the number of simultaneous competitors is usually small, one can employ MC+RV by model checking for this small number of competitors and runtime verify when the number of competitors is higher.

To apply MC+RV the developer must decide what to model check and what to runtime verify since she is the only one who knows the number of competitors that is rare enough not to warrant model checking. The formula that we must model check is the obvious one: if \( \beta \) represents a lack of race conditions and \( \alpha \) represents that the number of competitors is not exceedingly rare then we want to model check \( \alpha \rightarrow \beta \) and we runtime verify \( \alpha \).

To investigate this idea, we designed a variant of Peterson 2-process mutual exclusion algorithm [24] that prevents two processes from accessing a critical section simultaneously. In our version, there are \( N \) threads (for any \( N > 1 \)), and the goal is to guarantee mutual exclusion when the number of competitors for access to the critical section is \( \leq 2 \). The protocol is presented in Figure 1.

There, \( N \) processes run asynchronously. There is a global variable last that can take on
values between 1 and $N$ and denotes the id of the last process to wish to enter the critical section. Each process can be in one of four locations: location 0 (initial state) where it may idle forever or wish to enter the critical section, location 1 where it declares that it wishes to enter the critical section by setting $last$ to its own id, location 2 where it waits until no other process is competing or is in the critical section (i.e., all are in location 0) or that $last$ has changed since the process last modified it, hence, it is not the last one to enter the competition. In this case, the process can enter the critical section (location 3), after which it returns to its idle state.

We formally proved, using the method of invisible invariants \[25\] with the BDD-based model checker TLV that

$$\square \left( \sum_{i=1}^{N} at_{f_{1..3}}[i] \leq 2 \rightarrow \forall i,j. (i \neq j \rightarrow \neg (at_{f_{3}}[i] \land at_{f_{3}}[j])) \right)$$

where $at_{f_{k}}[i]$ denotes that the process whose id is $i$ is at location $k$. The summation term counts the processes whose location is in the range 1..3, i.e., the number of competitors.

Thus, as long as the number of competing threads is no greater than 2, mutual exclusion is guaranteed (via model checking). To identify all violations of mutual exclusion, we need only outfit the system with a runtime verifier that performs mutual exclusion detection when the number of threads is greater than 2\[1\].

Following the terminology of the previous subsections, here $\alpha$ is the property that the number of competitors is less than 2, and $\beta$ is the mutual exclusion property. We model check $\alpha \rightarrow \beta$ and we runtime verify $\alpha$.

### 4.3 Hardware Verification

\[26\] describes IDV, a system developed at Intel that enables simultaneous hardware logic design and verification. One of the keys to efficiency in this work is giving the developer the ability to provide the model checker with hints about the behavior of certain aspects of the design. For example, the developer can tell the model checker that two intermediate results are never simultaneously 1. This additional information helps prune the state space that must be verified; however, if the developer makes a mistake when providing the hints, the property that has been verified by the model checker may not hold on the actual system.

Developer-provided hints implicitly partition the state space of the system. Some of the state space can be used to model check high level properties of the design, and other portions

\[1\] It is interesting to note that the protocol of Figure 1 satisfies liveness, i.e., that $\forall i. \square (at_{f_{1}}[i] \rightarrow \Diamond at_{f_{3}}[i])$
of the state space are used to verify lower level properties. For those situations where the hints are beliefs about how the environment will interact with the system, it is natural to use runtime verification to verify the lower level properties—to analyze whether or not the developer-provided hints are satisfied by the environment. Sufficient testing will uncover any flaws in the developer-provided hints.

In this example, the partitioning of the space into that which is model checked and that which is runtime verified is a direct result of the hints the developer provides. If the developer asserts hints $\alpha_1, \ldots, \alpha_n$, and we want to model check the property $\beta$ then the model checking query we actually analyze is $\alpha_1 \land \cdots \land \alpha_n \rightarrow \beta$. The properties we verify at runtime are just $\alpha_1, \ldots, \alpha_n$.

### 4.4 Web Services

Many web applications today rely on external web services. For example, an e-commerce web site will validate a credit card number by consulting the appropriate company’s web service. Verifying that an e-commerce web site behaves appropriately is difficult because the code for external web services is unknown (and may change from the time the application is verified to the time it is deployed). Nevertheless, it is reasonable for a web developer to hope that verification technology can help give her confidence that her code behaves appropriately.

Here MC+RV is useful because model checking helps the developer verify that the web application is well-behaved assuming that the credit card company’s web service is well-behaved. For example, if the web service is supposed to return a response in 15 milliseconds, and the web application is a real-time system that relies on that fact, then it can be model checked to ensure its real-time properties hold so long as the credit card company’s service behaves as advertised. By runtime monitoring the credit card company’s web service, we can detect when it takes longer than 15 milliseconds and identify situations in which the overall application may miss its real-time constraints through no fault of its own.

In this example the partitioning of the system’s state space is based on the code available at the time of model-checking. The code that exists can be used for model checking; the remainder must be runtime verified. This kind of model checking requires additional information from the developer: a spec that dictates how the external web service is intended to behave. The model checking property of interest is again $\alpha \rightarrow \beta$, where $\alpha$ is the spec for the web service and $\beta$ is the high-level behavior of the web application. At runtime we simply verify $\alpha$ holds over each web service execution.

### 4.5 Repurposed Component

Many software applications delegate some low-level functionality to the operating system in which they are deployed. In Linux, for example, it is common to read the contents of a directory by executing the `ls` command. If the application interacts with the operating system by simply executing a system command written as a string, it may seem as though any possible OS command is being used. This is problematic from the perspective of model checking because now the entire state space of the application plus the entire state space of the operating system would need to be checked.

Such problems can sometimes be addressed with MC+RV. If through simple analysis or by developer intervention we learn that the application only uses one or two OS commands, we can model check the application and the implementations for those commands, without needing to model check the entire state space of the OS. We can then runtime verify that the application only invokes the OS commands that we model checked.
In this example, we model check $\alpha \rightarrow \beta$ where $\alpha$ says the only OS commands are those on the developer’s list, and $\beta$ is the application property of interest. At runtime we monitor $\alpha$: that the only OS commands executed are those from the list.

4.6 Exceptions

Computer systems can conceptually be broken into two kinds of code: exception-handling code and everything else. We often expect exception-handling code to be run infrequently; we also expect that many properties that we want to verify do not depend on exception-handling code. It is therefore reasonable to model check the system assuming no exceptions are triggered and to relegate the verification of the exception-handling code to runtime.

In this example, it is clear how to partition the state space into the parts that we model check and runtime verify. For model-checking, consider all paths through the code that produce no exceptions, and for run-time verification, place runtime verifiers only inside exception handling code. It is also clear that if we want property $\beta$ to hold that we model check $\beta$. What is less clear is how we runtime verify $\beta$ in the exception-handling code. If $\beta$ is a liveness property, run-time verification will not be possible. But even if $\beta$ is a safety property, we may need to outfit the application to store the pertinent history so that the exception-handling code has access to the right information when performing runtime verification.

5 Discussion

From a formal perspective, we can view MC+RV as an instantiation of assume-guarantee reasoning. Given a system $S$ and a property of interest $\beta$, an instance of MC+RV constructs a (safety) formula $\alpha$, model checks that $S$ guarantees $\beta$ assuming $\alpha$ and then runtime verifies $\alpha$. We know that any execution satisfying $\alpha$ is guaranteed to satisfy $\beta$ because of the model checker, but we do not know whether the remaining executions satisfy or falsify $\beta$. The runtime verifier identifies executions for which $\alpha$ is falsified and hence additional action must be taken, e.g., issue a warning, runtime verify $\beta$, or even verify some other property $\gamma$ related to $\beta$. Note that runtime verifying $\beta$ (or $\gamma$) may require having stored some history information in the state even before $\alpha$ is falsified.

A key observation about this formulation is that $\alpha$ must be a safety property, since it needs to be runtime verified; furthermore, runtime verifying $\alpha$ must be less expensive than runtime verifying $\beta$ itself since otherwise we could forego model checking entirely and simply verify $\beta$. In fact, $\alpha$ should be a formula that is the least expensive to verify out of all the candidate $\alpha$s. Each of the examples discussed earlier fits within this conceptual framework, and each example being distinguished in terms of what constitutes $\alpha$, what constitutes $\beta$, and what the application does if the runtime verifier discovers that $\alpha$ is falsified.

For software contracts (and web services), $\alpha$ dictates that the implementations of data structures (e.g., Heap) used within the application (e.g., curve-finding) are correct, and $\beta$ is just the property of interest. Thus for this example we model check that if the data structure implementation is correct ($\alpha$ is satisfied) then the application is correct ($\beta$ is satisfied), and we runtime verify that the data structure implementation is correct. If the runtime verifier discovers that $\alpha$ is falsified, we have found a bug in the data structure implementation, which is irrelevant for the purpose of curve-finding correctness.

For the race conditions example, $\alpha$ is the condition that the number of threads throughout the execution is 2 or fewer, and $\beta$ says there are no race conditions. Thus the model checker ensures that when the number of threads is 2 or fewer ($\alpha$), no race conditions hold ($\beta$), and
we runtime verify that there are 2 or fewer threads. When the runtime verifier discovers $\alpha$ is falsified, in this example we runtime verify $\beta$: that no race conditions occur.

For hardware verification, $\alpha$ is the conjunction of the developer-provided hints dictating invariants on parts of the system. $\beta$ is the property of interest. We model check that when the developer hints hold, the property of interest also holds, and we runtime verify that the hints are correct. If the runtime verifier discovers that $\alpha$ is falsified, we simply inform the developer that some of her hints were wrong, thereby suggesting that the hints be altered and the model checking rerun.

For the repurposed component, $\alpha$ dictates that only a handful of system calls are ever made by the program. $\beta$ is the property of interest. We therefore model check the system assuming that the only system calls are the ones we have identified, and we runtime verify that those are the only system calls the application makes. When $\alpha$ is violated at runtime, we know that $\beta$ may (or may not) be violated, and the appropriate action depends on the application.

For exceptions, $\alpha$ dictates that no exceptions are thrown (i.e., it is the negation of the exception conditions), and $\beta$ is the property of interest. We therefore model check the system assuming that no exceptions are thrown, and we runtime verify that no exceptions are thrown. When $\alpha$ is violated (exceptions are thrown), a potentially different formula $\gamma$ is likely checked, e.g., all the appropriate files are closed and the program terminates.

In the future, we plan to investigate automated techniques for generating $\alpha$ automatically, as well as optimizing the choice of $\alpha$ so as to minimize the cost of its runtime monitor while simultaneously minimizing the cost of model checking $\alpha \rightarrow \beta$. We can then utilize existing runtime verification techniques to automatically instrument the system $S$ to monitor $\alpha$ and existing techniques to model check $\alpha \rightarrow \beta$.

6 Related Work

Below we detail work that combines static and dynamic analysis of systems.

One line of work attempting to understand when and how we can verify properties of an entire state space while only exploring a portion of that space is the work on 3-valued model checking [17]. The key insight to this work is that if we know what we don’t know then sometimes we can infer what the possible things we don’t know could be and conclude that none of those possibilities could violate the specification. Of course, sometimes we simply fail to explore enough of the space for even 3-valued model checking to help us verify the system.

One class of related work utilizes static analysis to simplify runtime verification code e.g., [7]. After statically analyzing code to extract invariants, those invariants can demonstrate that particular runtime verification checks are unnecessary because the code enforces them directly.

Similarly [23] describes CCured, a C program transformation tool that attempts to impose a statically-checked type system on C programs. Those elements of a program that cannot be statically type-checked are outfitted with runtime monitors to catch memory errors. This work could be construed as using static techniques to simplify runtime checks as in [7]; however, the focus here is just the reverse: that the runtime checks are used only when the static analysis is insufficient.

Another line of work, e.g., [5, 9, 15, 20, 28], combines model generation, model checking, and testing (which can be construed as a kind of runtime verification) to achieve better coverage of the system’s state space and find bugs or provide proofs of verification.
7 Conclusion

We propose investigating the theory of integrating model checking and runtime verification, with the eventual goal of providing a single framework for exporting verification technology to the outside world. From the perspective of model checking, MC+RV broadens the applicability of model checking to include systems that are too large or too dynamic to be completely explored. The downside is that the entire system cannot be model checked before it is deployed, and hence verification failures may not be found until after deployment. From the perspective of runtime verification, MC+RV expands the class of properties that can be verified and reduces the overhead of runtime verification. The downside is that the MC step may find bugs that are irrelevant to the deployments of interest and incurs offline costs. MC+RV therefore has drawbacks as well as benefits; hence, we envision the theory of MC+RV resulting in controls for the developer to choose the appropriate mix of model checking and runtime verification. For those situations where offline costs must be small, the developer can dictate the model checking component only consider a small portion of the state space, and for those situations where verification before deployment is important, the developer can dictate that only those components of the system not known at the time of analysis should be runtime verified. MC+RV will allow developers to choose the verification approach best-suited for their application by properly balancing the tradeoffs of model checking and runtime verification.

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