

15-18 JULY • LAUSANNE • SWITZERLAND

CALL FOR PAPERS

The 2019 edition of the International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD) will be held in Lausanne, Switzerland, as a forum devoted to modeling, simulation and synthesis for Analog, Mixed-signal, RF (AMS/RF) and multi-domain (nanoelectronics, biological, MEMs, optoelectronics, etc.) integrated circuits and systems. Experiences with modeling, simulation and synthesis techniques in diverse application areas are also welcomed. Objective technologies include CMOS, beyond CMOS, and, More-than-Moore such as MEMs, power devices, sensors, passives, etc. Conference proceedings will be submitted for IEEExplore, and, a selection of papers will be considered for a special issue of Integration, the VLSI Journal, Elsevier. Topics of interest include:

- · Compact modeling · Performance and behavioral modeling
- · Power and thermal modeling
- · Reliability and variability modeling
- · Multi-domain modeling
- · RF/microwave/mm-wave modeling
- · Modeling languages and applications
- · Linear and non-linear model order reduction
- · Circuit & system theory and application
- · Fault modeling

Simulation, Verification and Test

- · Behavioral simulation
- · Numerical and symbolic simulation methods
- · Simulation and estimation methods of variability and yield
- · Analysis of reliability effects (aging, electromigration, stress...)
- · High-frequency simulation techniques
- · Multilevel simulation techniques
- · Multi-domain simulation
- · Formal and functional verification
- · Fault simulation and diagnosis

Synthesis

- Multilevel design methodologies
- · Synthesis methods of multi-domain circuit and systems
- · Physical synthesis
- · High-frequency circuit and system design
- · Low-power design techniques
- · Parasitic-aware synthesis
- · Variability-aware & reliability-aware design
- Performance monitors & self-healing techniques
- · Test and design-for-test methods
- · Fault tolerance
- · Language-based synthesis techniques
- · Optimization methods applied to circuit and system design

Applications of Modeling, Simulation and Synthesis in

- · Automotive
- **Bio-electronics**
- Security
- · Internet-of-things
- Communications
- · Renewable systems
- · Energy management
- · Engineering education

SMACD 2019 IS TECHNICALLY CO-SPONSORED







IMPORTANT DATES

Paper Submission Deadline Extended	22 th March 2019
Author Notification	19 ^h April 2019
Camera Ready Paper Submission	3 th May 2019
Early Registration Deadline	3 th May 2019

ORGANIZED BY







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EPFL, École polytechnique fédérale de Lausanne Route Cantonale, 1015 Lausanne, Switzerland

CO-LOCATED WITH PRIME 2019





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