

CNF From Interpolants Via BDDs

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Trade &

NSW

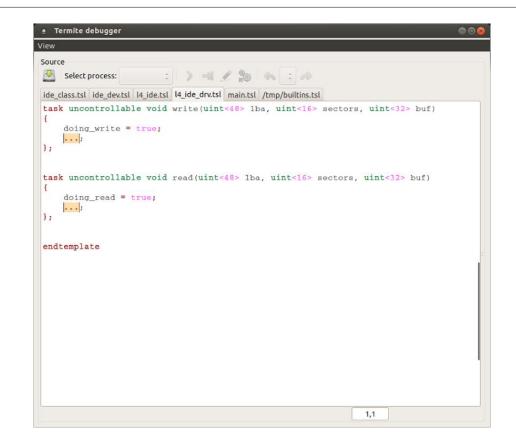




- Driver Synthesis
 - See CAV paper
- Strategy Extraction
 - Nina's talk yesterday

Driver Synthesis



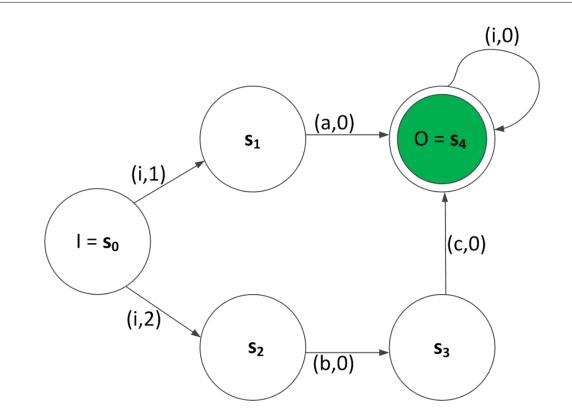


Driver Synthesis

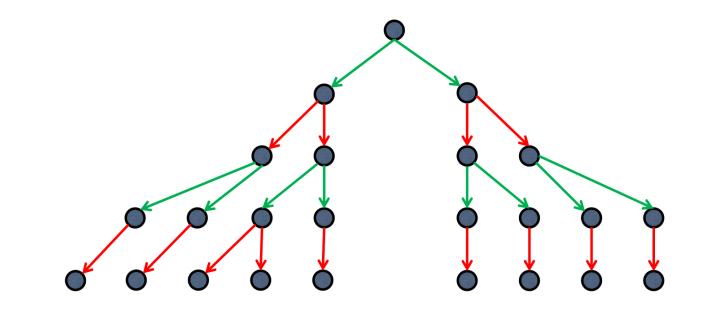


'iew	
Sour	re
(CTE)	
	Select process:
ide_	class.tsl ide_dev.tsl l4_ide.tsl l4_ide_drv.tsl main.tsl /tmp/builtins.tsl
tas	k uncontrollable void write(uint<48> lba, uint<16> sectors, uint<32> buf)
{	
	doing write = true;
	dev.rcmd write dev(/*any value*/6'h0 ++ 1'h1 ++ /*any value*/1'h0);
	dev.rcmd write lba high0(os.r lba[40:47]);
	dev.rcmd write lba high0(os.r_lba[16:23]);
	dev.rcmd_write_lba_mid0(os.r_lba[32:39]);
	dev.rcmd write lba mid0(os.r lba[8:15]);
	dev.rcmd write lba low0(os.r lba[24:31]);
	dev.rcmd write lba low0(os.r lba[0:7]);
	dev.rcmd write sectors(os.r sectors[8:15]);
	dev.rcmd write sectors(os.r sectors[0:7]);
	dev.rcmd_write_errcmd(8'h35);
	dev.rdma_write_command(1'h0 ++ /*any value*/7'h0);
	dev.fill prd(os.r buf, dev.reg sectors ++ dev.reg sectors1);
	dev.rdma write command(1'h1 ++ /*any value*/2'h0 ++ 1'h0 ++ /*any value*/4'h0);
};	
tas	k uncontrollable void read(uint<48> lba, uint<16> sectors, uint<32> buf)
{	
	doing_read = true;
	<pre>dev.rcmd_write_dev(/*any value*/6'h0 ++ 1'h1 ++ /*any value*/1'h0);</pre>
	<pre>dev.rcmd_write_lba_high0(os.r_lba[40:47]);</pre>
	<pre>dev.rcmd_write_lba_high0(os.r_lba[16:23]);</pre>
	<pre>dev.rcmd_write_lba_mid0(os.r_lba[32:39]);</pre>
	<pre>dev.rcmd_write_lba_mid0(os.r_lba[8:15]);</pre>
	<pre>dev.rcmd_write_lba_low0(os.r_lba[24:31]);</pre>
	<pre>dev.rcmd_write_lba_low0(os.r_lba[0:7]);</pre>
	<pre>dev.rcmd_write_sectors(os.r_sectors[8:15]);</pre>
	dev romd write sectors (ne r sectors [1.7]).

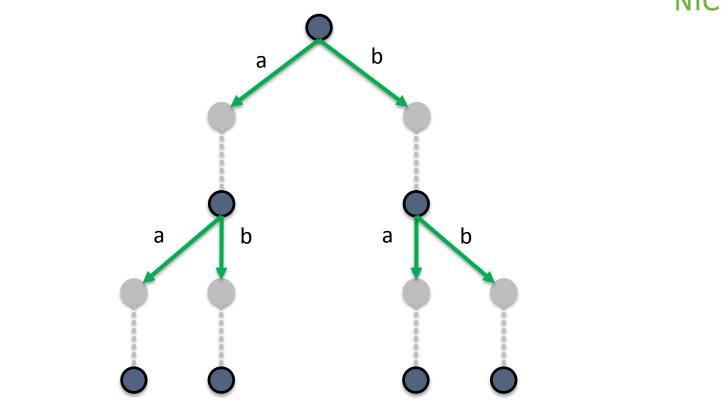








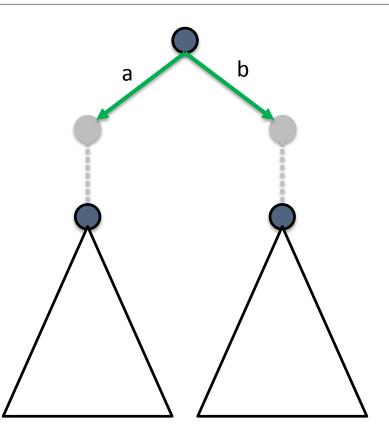
controllable moveuncontrollable move





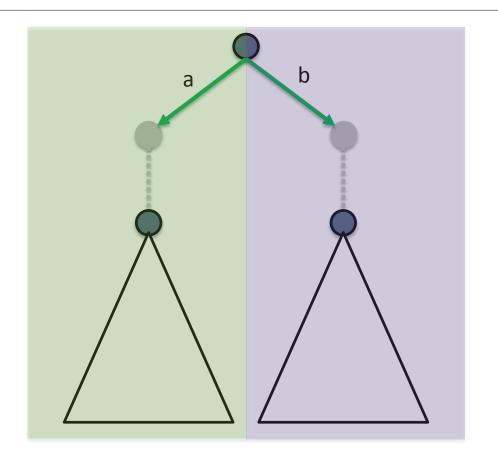
State Partitioning





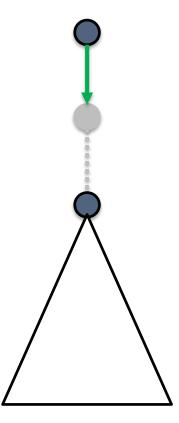
State Partitioning





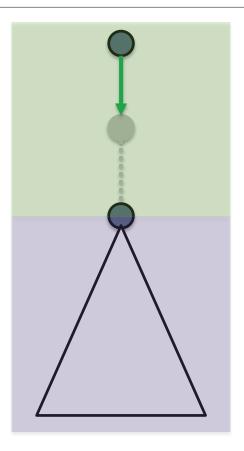
Next State Operation





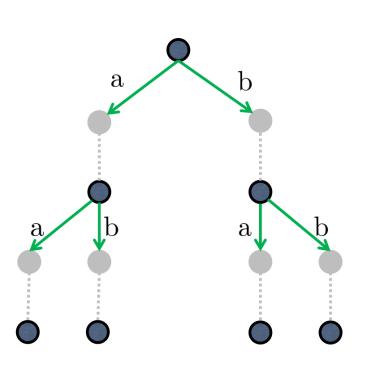
Next State Operation





Strategy Extraction

- 1) Our interpolants get reused
 - We need small interpolants
 - We need small CNF
- 2) Our interpolants are over small sets of variables
 - Interpolants are state sets (over state variables)
 - An efficient representation exists



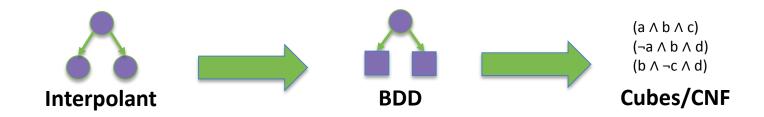




- BDDs provide efficient representation
 - Interpolants are redundant and potentially large
 - BDDs are canonical
- CNF from BDD is simple and efficient
 - Get the shortest path to False
 - Block that path and repeat
- BDDs can explode
 - Small number of variables

Experimental Set Up



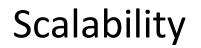






	Average	Maximum
Interpolant Size	67.66 nodes	1410 nodes
Interpolant Time	0.24 sec	1.50 sec
BDD Size	14.8 nodes	58 nodes
BDD Time	< 0.01 sec	< 0.01 sec
Cube Size	2.05 clauses	12 clauses
Cube Time	< 0.01 sec	< 0.01 sec

Runs of EvaSolver: 36 Total Interpolants: 872





- Time spent solving 1297.37s
- Time spent on interpolants 232.33s (17%)

- BDDs do not contribute significantly to time
- Interpolant size increases with state space

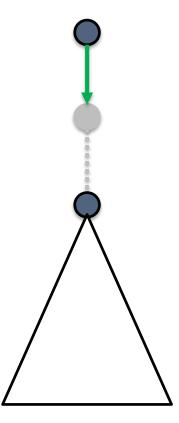
Related Work



- ALLSAT
 - Alternative to interpolation
- Sweeping
 - Reduces circuit
 - Doesn't give CNF
- Interpolants in CNF (CAV '13)
 - Needs domain specific solution

Next State Operation





ALLSAT



- Existential quantification
 - Find solution (via SAT)
 - Block solution
 - Repeat



ALLSAT



- Existential quantification
 - Find solution (via SAT)
 - Block solution
 - Repeat
- Problem:

 $(x \land x') \qquad (\neg x \land \neg x')$ After Projection: (x') $(\neg x')$

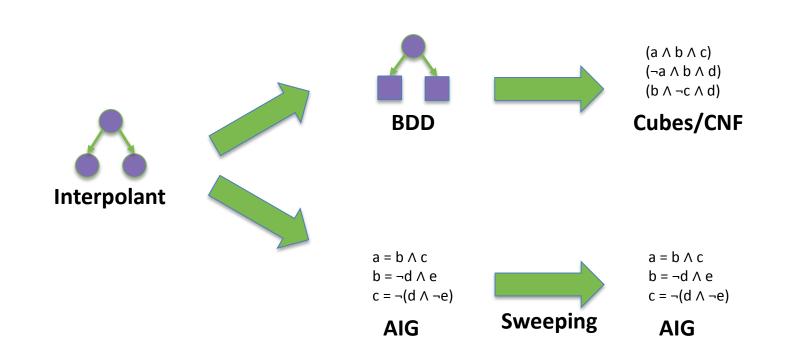
Related Work



- ALLSAT
 - Alternative to interpolation
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Experimental Set Up





Results



	Average	Maximum
Interpolant Size	67.66 nodes	1410 nodes
Interpolant Time	0.24 sec	1.50 sec
Cube Size	2.05 clauses	12 clauses
Cube Time	< 0.01 sec	< 0.01 sec
Post-sweeping Size	14.89 nodes	80 nodes
Sweeping Time	< 0.01 sec	< 0.01 sec

Runs of EvaSolver: 36 Total Interpolants: 872

Third Party Libraries

NICTA

- PeRIPLO (University of Lagano)
 - Interpolant library
 - Backed by MiniSAT
 - Performs some redundancy detection
- CUDD (CU Boulder)
 - BDD library
- Ilmc (CU Boulder)
 - Model checking library
 - Sweeping algorithms (BDD, SAT, Cut)

Other Talks



12:35 pm, July 21st, CAV N. Narodytska, A. Legg, F. Bacchus, L. Ryzhyk and A. Walker Solving Games without Controllable Predecessor

14:50 pm, July 21st, CAV P. Cerny, T. Henzinger, A. Radhakrishna, L. Ryzhyk and T. Tarrach Regression-free Synthesis for Concurrency

09:00 am, July 24th, SYNT Leonid Ryzhyk Automatic Device Driver Synthesis Project (Invited Talk, OSDI'14)



